

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

1-32. (cancelled)

33. (currently amended) A self test system for a medical device, comprising:

a plurality of components each of which has a respective self-test means associated therewith adapted to carry out a self-test routine on the associated component,

wherein each of said self-test means is limited to generation of self-testing signals and is adapted to be activated independently of operation of the medical device and not by a signal from a processor associated with said medical device, with results thereof being passed to a common processor.

34. (previously presented) A self test system for a medical device according to claim 33, wherein the self test is adapted to be activated independently of operation of the medical device and not by a signal from the common processor associated with said medical device.

35. (previously presented) A self test system for a medical device according to claim 33, wherein the self test system includes:

a summator adapted to receive data from the one or more self test means associated with said components, the summator storing said data; and one of:

an indicator to which said data can be transmitted directly; or

a processor which can access said data.

36. (previously presented) A self test system for a medical device, said medical device being arranged to transmit information concerning components of said medical device, comprising:

one or more self test units adapted to independently self test one or more individual components of the medical device; and

a summator adapted to receive data from the one or more self test units associated with said components, the summator storing said data; and one of:

an indicator to which said data can be transmitted directly, the indicator being adapted to show the status of the components when tested; or

a processor which can access said data.

37. (previously presented) A self test system according to claim 33, wherein the plurality of components each has a dedicated self test unit.

38. (previously presented) A self test system according to claim 33, wherein one or more of the plurality of components communicate with a self test unit.

39. (previously presented) A self test system according to claim 33, further comprising a single data link, wherein the single data link is adapted to feed data from the self test to a summator.

40. (previously presented) A self test system according to claim 33, wherein data from the self test units is fed by a plurality of separate data links to a summator.

41. (previously presented) A self test system according to claim 34, wherein the summator is a separate counter/adder component, or a micro-controller.

42. (previously presented) A self test system according to claim 34, wherein the summator is or includes a subtractor component.

43. (previously presented) A self test system according to claim 34, further comprising a main microprocessor, wherein the summator is part of the main microprocessor.

44. (previously presented) A self test system according to claim 34, wherein data from each component is delivered to the summator as a signal comprising a number of pulses.

45. (previously presented) A self test system according to claim 44, wherein the pulses are identified as discrete numbers of pulses to a value of x^2 , where x is a whole number, or as a prime number.

46. (previously presented) A self test system according to claim 44, wherein the self test system comprises an AED, and a number of pulses for the following components making up the AED are any of the following:

electrode condition	$32^2 = 1024$ pulses
battery on charge	$33^2 = 1089$ pulses
system condition	$34^2 = 1156$ pulses
modem condition	$35^2 = 1225$ pulses.

47. (previously presented) A self test system according to claim 33, wherein a self test for a component is triggered via

triggering means by a test having been carried out on another component.

48. (previously presented) A self test system according to claim 33, further comprising a digital signal processor adapted to activate the test.

49. (previously presented) A self test system according to claim 48, further comprising a server of base station, wherein the digital signal processor is activated by a signal from the server or base station in contact with the medical device.

50. (previously presented) A self test system according to claim 33, further comprising a base station for said device, wherein the self test is activated by the medical device being placed in the base station for said device.

51. (previously presented) A self test system according to claim 33, further comprising circuitry of said device, wherein a self test involves testing a voltage across substantially all of the circuitry of the medical device.

52. (previously presented) A self test system according to claim 48, wherein the test is carried out either at a first voltage of 450V or a second voltage of 40V.

53. (previously presented) A self test system according to claim 33, further comprising an indicator and a summator, wherein the indicator is a digital display adapted to display results for the summator or processor independently of one another.

54. (previously presented) A self test system according to claim 53, wherein the processor reviews the results of the summator prior to the results being fed to the indicator.

55. (previously presented) A self test system according to claim 53, wherein a number of pulses being fed to the summator is recorded.

56. (previously presented) A self test system according to claim 55, wherein the number of pulses is measured against set parameters to provide an indication of whether one or more components are functioning as required.

57. (previously presented) A self test system according to claim 33, wherein the self test system tests the power source for the medical device, prior to testing other components.

58. (previously presented) A self test system according to claim 33, wherein the testing of components is based on testing a sample of signals over time or testing a defined number of signals for each component.

59. (previously presented) A self test system according to claim 33, wherein an indicator is integral with said medical device.

60. (previously presented) A self test system according to claim 33, wherein an indicator is a separate component associated with said medical device.

61. (previously presented) A method of self testing a medical device, said medical device being arranged to transmit information concerning components of said medical device to an indicator, comprising:

carrying out a self test of one or more components independent of operation of the medical device;

storing results of the self test; and

on operation of said medical device, transmitting said results to a processor for analysis and display by said indicator.

62. (previously presented) A method according to claim 61, further comprising activating a timing device so that the self test is a periodic or aperiodic self test.

63. (previously presented) A method according to claim 61, further comprising sending information concerning said components to a summator prior to being sent to a processor.